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Amy Jonsson

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of Inventor:  
**Jade M. Kizer**

Application No. **10/645,201**

Filed: **21 August 2003**

Title: **Periodic Interface Calibration for  
High Speed Communication**

Group Art Unit: *Unknown*

Examiner: *Unknown*

**CUSTOMER NO. 38342**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

**UNDER 37 C.F.R. §1.56**

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is a Form PTO-SB/08. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. The exception to this, in accordance with the 05 August 2003 Official Gazette Notice in which the USPTO waives the requirement for submitting copies of U.S. Patent and Publications for cases filed after 30 June 2003, is that copies of U.S. Patent documents and copies of U.S. Patent publications are not being submitted.

This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed before the mailing date of the first Office Action on the merits.

***Fee Authorization.*** The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (RBUS 1305-1). A copy of this authorization is enclosed.

Respectfully submitted,

Date: 16 Dec 03

By: Mark A. Haynes

Mark A. Haynes, Reg. No. 30,846

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Substitute for form 1449/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**(Use as many sheets as necessary)**

**C mplete if Known**

Application Number	10/645,201
Filing Date	21 August 2003
First Named Inventor	Jade M. Kizer
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RBUS 1305-1

Sheet	1	of	3
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## U. S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

[illegible]

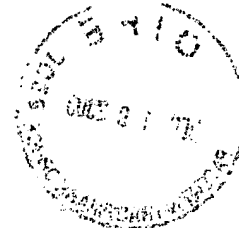
Examiner Signature		Date Considered	
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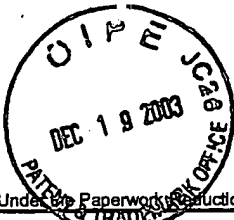
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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PTO/SB/08B (08-03)

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	10/645,201		
		Filing Date	21 August 2003		
		First Named Inventor	Jade M. Kizer		
		Art Unit	Unknown		
		Examiner Name	Unknown		
Sheet	2	of	3	Attorney Docket Number	RBUS 1305-1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	C1 ✓	SLDRAM Inc. "400 Mb/s/pin SLDRAM" 4M x 18 SLDRAM, July 9, 1998, pp. 1-69	
	C2 ✓	Peter Gillingham "SLDRAM Architectural and Functional Overview" August 29, 1997, pp. 1-14	
	C3 ✓	Peter Gillingham et al. "SLDRAM: High-Performance, Open-Standard Memory" IEEE November/December 1997, pp. 29-39	
	C4 ✓	Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society "Draft Standard for a High-Speed Memory Interface (SyncLink)" 1996 pp. 1-56	
	C5 ✓	Direct Rambus Short Channel Layout Guide August 2001, pp. 1-40	
	C6 ✓	Direct Rambus Long Channel Design Guide, 2000, pp. 1-45	
	C7	How to Measure RDRAM* System Clock Jitter, Intel June 1999, pp. 1-15	
	C8	Lluis Paris et al. "WP 24.3 A 800MB/s 72 Mb SLDRAM with Digitally-Calibrated DLL" IEEE International Solid-State Circuits Conference 1999,, pp 1-5	
	C9	Yasunobu Nakase et al. "Source-Synchronization and Timing Vernier Techniques for 1.2 GB/s SLDRAM Interface" IEEE Journal of Solid-State Circuits, Vol. 34, No. 4, April 1999	
	C10	RDRAM Direct Rambus Clock Generator, April 2002, pp. 1-22	

Examiner Signature	Date Considered
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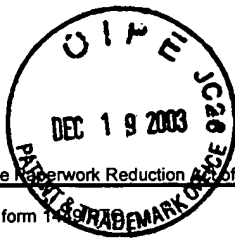
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number 10/645,201

Filing Dat 21 August 2003

First Named Inventor Jade M. Kizer

Art Unit Unknown

Examiner Name Unknown

Attorney Docket Number RBUS 1305-1

Sheet 3 of 3

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	C11	Lluis Paris et al. "WP 24.3: An 800MB/s 72 Mb SLDRAM with Digitally-Calibrated DLL" ISSCC Slide Supplement IEEE 1999, pp. 352-353	

Examiner  
SignatureDate  
Considered

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